

I2S to ADxxxx and PCMxx DACs

OFFTIME = 1.1337868480725623582766439909297e-5
 ONTIME = 1.1337868480725623582766439909297e-5
 DELAY = 5.000us
 STARTVAL = 0
 OPVAL = 1
 LE_OUT[0] → LE0
 $FSYNC = (1 / F_s)$
 32 Bit WORD 44.1KHz
 OFFTIME = 1.1337868480725623582766439909297e-5
 ONTIME = 1.1337868480725623582766439909297e-5
 DELAY = 5.000us
 STARTVAL = 1
 OPVAL = 0
 LE_OUT[1] → LE1
 OFFTIME = 1.7715419501133786848072562358276e-7
 ONTIME = 1.7715419501133786848072562358276e-7
 DELAY = 5us
 STARTVAL = 0
 OPVAL = 1
 SCK_OUT[0] → SCK0
 $SCK = 1 / (64 \times F_s)$
 32 Bit WORD 44.1KHz
 OFFTIME = 1.7715419501133786848072562358276e-7
 ONTIME = 1.7715419501133786848072562358276e-7
 DELAY = 5us
 STARTVAL = 1
 OPVAL = 0
 SCK_OUT[1] → SCK1
 OFFTIME = 3.543083900226757369614512471654e-7
 ONTIME = 3.543083900226757369614512471654e-7
 DELAY = 53.543083900226757369614512471654e-7
 STARTVAL = 0
 OPVAL = 1
 DATA_OUT[0] → DATA0
 $DATA = 2 / (64 \times F_s)$
 32 Bit WORD 44.1KHz
 OFFTIME = 3.543083900226757369614512471654e-7
 ONTIME = 3.543083900226757369614512471654e-7
 DELAY = 53.543083900226757369614512471654e-7
 STARTVAL = 1
 OPVAL = 0
 DATA_OUT[1] → DATA1

