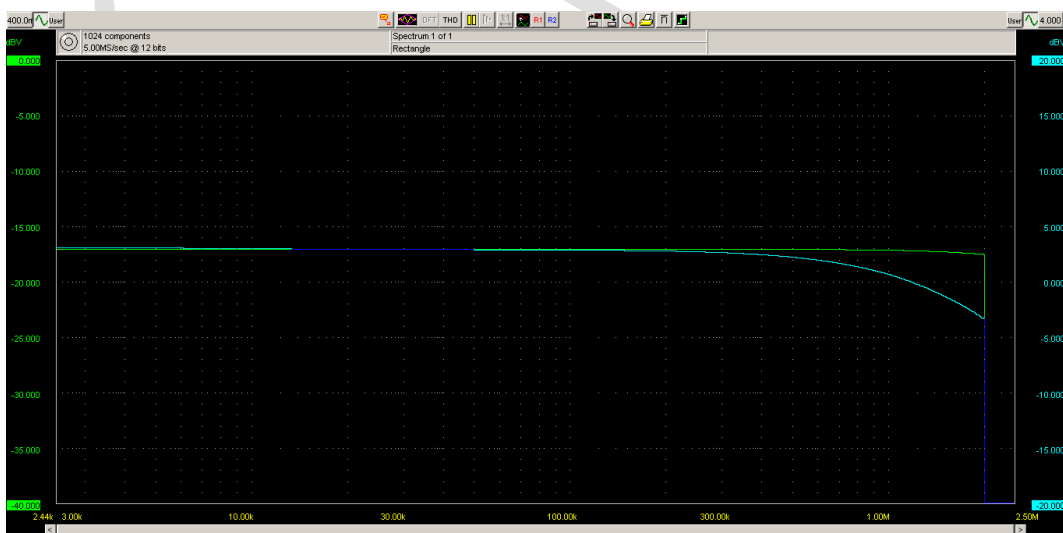
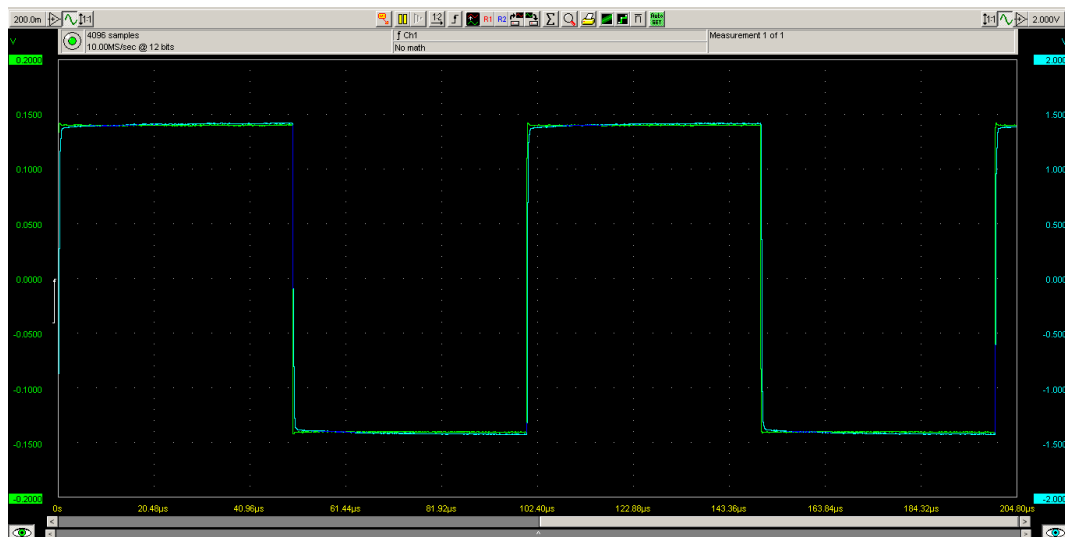


Vero Protos

The All-FET NFB version was also built on PCBs. And the measurements also corresponded to simulations. No trimming was actually required, such was the accurate prediction of the bias resistor values. And the DC offset stay well within 5mV over a period of an hour, after 5 minutes of initial warm up. -3dB bandwidth was 1.1MHz, which agreed almost exactly to Spice prediction. Output impedance is now very low at 0.1ohm.



NFB Proto 3 Frequency Response 0.14V Sine Wave at Input



NFB Proto 3 10kHz 0.14V Square Wave at Input



NFB Proto 3 10kHz 0.14V Sine Wave at Input

Appendix 1a UTHAiM Basic Schematics (4-FET current mirrors)

Note that connections between G & D of Q3,5 requires hard wire jumpers at the back of the PCB.

